

U.S. Department of Commerce, Patent and Trademark	Atty. Docket No.	Application No.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	SN DK.A06US5	09/759,119
	Applicants	Conf. No.
(Use several sheets if necessary)	Harari	1201
(Form PTO-1449)	Filing Date	Art Group
	1/11/01	2827

#### U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	1	4,752,871	6/21/88	Sparks et al.			
	2	4,691,298	9/1/87	Fukuda et al.			
	3	4,434,479	2/28/84	Chen et al.			
	6	4,763,305	8/9/88	Kuo			
	5	4,890,259	12/26/89	Simko			

#### Foreign Patent Documents

							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	6	EP1108681A2	6/20/01	EPO				
	7	EP0176111A1	4/2/86	EPO				
	6	GB2029154A	3/12/80	Great Britain				
	6	DE2828855A	1/3/80	Germany				X
	10	EP0349775B1	1/10/90	EPO				
	11	WO 90/12400	10/18/90	PCT				
	12	EP0778582B1	11/14/01	EPO				
	13	DE69033862T	11/14/01	Germany				X
	10	DE69030959T	11/14/01	Germany				X
	15	EP0539358B1	5/5/93	EPO				

#### OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

16	Nullity of the German Part of the European Patent EP 0778582B1 (DE 690 30 959), received from the German Patent Office on May 1, 2007, 40 pages.
17	Nullity of the German Part of the European Patent EP 0778582B1 (DE 690 30 959), English Translation, received from the German Patent Office on May 1, 2007, 40 pages.
18	Kupec et al., "Triple Level Poly Silicon E2PROM with Single Transistor Per Bit," <i>IEEE International Electron Device Meeting</i> , Technical Digest, 8-10 December 1980, pp, 602-605.
19	Gastaldi et al., "A 1-Mbit EMOS EPROM with Enhanced Verification," <i>IEEE Journal of Solid State Circuits</i> , Vol. 23, No. 5, October 1998, pp. 1150-1156.

Examiner

Date Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.